

EAST SEARCH

1/9/2008

L#	Hits	Search String	Databases
S17	129	S6 and ((hardware or resource) with type)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S1	25105	(processor or computer or "logic unit" or logic) with (parallel or concurrent) with (processing or e	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S31	215	S6 and (((hardware or resource)) with (time or occupancy or utilization))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S42	288	S40 or S41	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S25	54	S6 and (deadlock or deadlocked or deadlocking)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S8	151	S6 and ((allocate or allocated or allocating or allocation or assign or assigned or resource))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S7	73	S6 and ((request or requested or requesting) with (hardware or resource))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S16	66	S6 and ((repeat or repeated or repeating or repeatedly or recursively) with (control	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S21	27	S6 and ((resource with (manager or controller)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S20	63	S6 and (thread with (manager or controller))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S6	554	S2 and S5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S36	2	S6 and ((compare or compared or comparison) with simulation with (result or outpu	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S3	30472	(processor or computer or "logic unit" or logic) with (parallel or concurrent) with (processing or e	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S15	89	S6 and ((repeat or repeated or repeatedly or recursively or resource)) with (request c	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S38	149	S6 and ((compare or compared or comparing or comparison) with (result or output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S10	76	S6 and ((allocate or allocated or allocating or allocation or assign or assigned or ass	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S18	50	S6 and ((hardware or resource) with (dependency or dependent))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S29	15	S6 and ((detect or detected or detecting or detection or identify or identifying or ident	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S28	4	S6 and ((competition or competing) with (write or read or input or output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S33	2	S6 and (thread with (processing or execution or operation) with time with (limit or constraint))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S5	4714	S4 and (simulate or simulated or simulating or simulation)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S34	5	S6 and (thread with time with (limit or constraint))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S22	247	S6 and ((lower or higher) with (hierarchy or level))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S11	20	S6 and ((allocate or allocated or allocating or allocation or assign or assigned or ass	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S26	2	S6 and ((monitor or monitored or monitoring) with (write or read or input or output) with request)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S41	269	S39 and S40	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S13	114	S6 and ((parallel or concurrent) with thread)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S14	72	S6 and ((control or controlled or controlling) with thread with (processing or execution or operatic	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S24	1	S6 and (request with deadlock)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S32	123	S6 and (thread with (processing or execution or operation) with time)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S23	20	S6 and ((monitor or monitored or monitoring) with request)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S27	150	S6 and ((monitor or monitored or monitoring) with (write or read or input or output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S4	34387	S2 or S3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S39	472	S8 or S13 or S15 or S17 or S19 or S22 or S27 or S31 or S32 or S38	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S30	2	S6 and ((budget or budgeted or budgeting) with (time or occupancy or utilization))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S35	7	S6 and ((processing or execution or operation) with time with (limit or constraint))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S40	288	S7 or S9 or S10 or S11 or S12 or S14 or S16 or S18 or S20 or S21 or S23 or S24 or S25 or S2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S2	4757	(multi-threaded or multithreaded or ((multiple or plurality) near2 (thread or threaded))) with (procx	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S19	208	S6 and ((hardware or resource) with (hierarchy or hierarchical or hierarchized or hierarchize or h	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S12	43	S6 and ((sequential or serial) with thread)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

S37	2	S6 and ((compare or compared or comparison) with (estimated or expected) with (US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S9	7	S6 and ((allocate or allocating or allocation or assign or assigned or assigning or as (US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S43	4757	(multi-threaded or multithreaded or ((multiple or plurality) near2 (thread or threaded))) with (proc US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S44	30472	(processor or computer or "logic unit" or logic) with (parallel or concurrent) with (processing or e US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S45	34387	S43 or S44
S46	4714	S45 and (simulate or simulated or simulating or simulation)
S47	554	S43 and S46
S50	76	S47 and ((allocate or allocating or allocation or assign or assigned or assigning or as US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S48	73	S47 and ((request or requested or requesting) with (hardware or resource))
S49	7	S47 and ((allocate or allocating or allocation or assign or assigned or assigning or as US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S52	43	S47 and ((sequential or serial) with thread)
S51	20	S47 and ((allocate or allocating or allocation or assign or assigned or assigning or as US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S53	72	S47 and ((control or controlled or controlling) with thread with (processing or execution or operat US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S54	66	S47 and ((repeat or repeated or repeating or repeatedly or recursively) with (control US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S57	27	S47 and ((resource with (manager or controller))
S55	50	S47 and ((hardware or resource) with (dependency or dependent))
S56	63	S47 and ((thread with (manager or controller))
S59	1	S47 and ((request with deadlock)
S60	54	S47 and ((deadlock or deadlocked or deadlocking)
S63	15	S47 and ((detect or detected or detecting or detection or identify or identified or identifying or idei US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S61	2	S47 and ((monitor or monitored or monitoring) with (write or read or input or output) with request US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S62	4	S47 and ((competition or competing) with (write or read or input or output))
S65	2	S47 and ((thread with (processing or execution or operation) with time with (limit or constraint))
S64	2	S47 and ((budget or budgeted or budgeting) with (time or occupancy or utilization))
S66	5	S47 and ((thread with time with (limit or constraint))
S67	7	S47 and ((processing or execution or operation) with time with (limit or constraint))
S68	2	S47 and ((compare or compared or comparison) with simulation with (res US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S69	2	S47 and ((compare or compared or comparing or comparison) with (estimated or expected) with US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S70	288	S48 or S49 or S50 or S51 or S52 or S53 or S54 or S55 or S56 or S57 or S58 or S59 or S60 or : US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S58	20	S47 and ((monitor or monitored or monitoring) with request)
S71	151	S47 and ((allocate or allocating or allocation or assign or assigned or assigning or as US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S77	150	S47 and ((monitor or monitored or monitoring) with (write or read or input or output))
S78	215	S47 and (((hardware or resource)) with (time or occupancy or utilization))
S80	149	S47 and ((compare or compared or comparing or comparison) with (result or output))
S81	472	S71 or S72 or S73 or S74 or S75 or S76 or S77 or S78 or S79 or S80
S82	288	S48 or S49 or S50 or S51 or S52 or S53 or S54 or S55 or S56 or S57 or S58 or S59 or S60 or : US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S83	269	S81 and S82
S72	114	S47 and ((parallel or concurrent) with thread)
S73	89	S47 and ((repeat or repeated or repeating or repeatedly or recursive or recursively) with (request US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S74	129	S47 and ((hardware or resource) with type)
S75	208	S47 and ((hardware or resource) with (hierarchy or hierarchical or hierarchize or hierarchized or hierarchized or hierarchical or level))
S76	247	S47 and ((lower or higher) with (hierarchy or level))
S79	123	S47 and ((thread with (processing or execution or operation) with time)
S84	288	S82 or S83
S85	4764	(multi-threaded or multithreaded or ((multiple or plurality) near2 (thread or threaded))) with (proc US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S86	30507	(processor or computer or "logic unit" or logic) with (parallel or concurrent) with (processing or e US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S87	34427	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

S88	4726	S87 and (simulate or simulated or simulating or simulation)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S89	7	S89 and (allocate or allocated or allocating or assign or assigned or assigning or as	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S89	557	S85 and S88	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S90	73	S89 and ((request or requested or requesting) with (hardware or resource))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S93	20	S89 and (allocate or allocated or allocating or allocation or assign or assigned or assigning or as	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S92	76	S89 and (allocate or allocated or allocating or allocation or assign or assigned or assigning or as	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S95	72	S89 and (control or controlled or controlling) with thread with (processing or execution or operat	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S94	43	S89 and ((sequential or serial) with thread)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S98	63	S89 and (thread with (manager or controller))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S96	66	S89 and ((repeat or repeated or repeating or repeatedly or recursively) with (control	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S97	50	S89 and ((hardware or resource) with (dependency or dependent))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S100	20	S89 and ((monitor or monitored or monitoring) with request)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S99	27	S89 and (resource with (manager or controller))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S103	2	S89 and ((monitor or monitored or monitoring) with (write or read or input or output) with request	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S101	1	S89 and (request with deadlock)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S102	54	S89 and (deadlock or deadlocked or deadlocking)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S106	2	S89 and ((budget or budgeted or budgeting) with (time or occupancy or utilization))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S104	4	S89 and ((competition or competing) with (write or read or input or output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S108	5	S89 and (thread with time with (limit or constraint))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S105	15	S89 and ((detect or detected or detecting or detection or identify or identified or identifying or ide	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S110	2	S89 and ((compare or compared or comparing or comparison) with simulation with (result or out	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S107	2	S89 and (thread with (processing or execution or operation) with time with (limit or constraint))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S109	7	S89 and ((processing or execution or operation) with time with (limit or constraint))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S112	152	S89 and ((allocate or allocated or allocating or allocation or assign or assigned or assigning or as	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S111	2	S89 and ((compare or compared or comparing or comparison) with (estimated or expected) with	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S113	114	S89 and ((parallel or concurrent) with thread)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S114	89	S89 and ((repeat or repeated or repeating or repeatedly or recursively) with (request	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S117	248	S89 and ((lower or higher) with (hierarchy or level))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S115	129	S89 and ((hardware or resource) with type)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S116	209	S89 and ((hardware or resource) with (hierarchy or hierarchical or hierarchized or hierarchize or	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S120	123	S89 and (thread with (processing or execution or operation) with time)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S118	150	S89 and ((monitor or monitored or monitoring) with (write or read or input or output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S122	474	S112 or S113 or S114 or S115 or S116 or S117 or S118 or S119 or S120 or S121	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S119	217	S89 and (((hardware or resource)) with (time or occupancy or utilization))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S123	288	S90 or S91 or S92 or S93 or S94 or S95 or S96 or S97 or S98 or S99 or S100 or S101 or S102	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S121	150	S89 and ((compare or compared or comparing or comparison) with (result or output))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S125	288	S123 or S124	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S124	269	S122 and S123	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S127	6	S125 and S126	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S126	17	S89 and ((detect or detected or detecting or detection or identify or identified or identifying or ide	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S128	299	S123 or S126	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S129	276	S122 and S128	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S130	299	S128 or S129	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB
S131	17	S130 and S126	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

EAST SEARCH

1/9/2008

Results of search set \$115

Document Kind Codes Title	Issue Date	Current OR	Abstract
US 20080005112 A1 Predictive log synchronization	20080103 707/8		
US 20070300227 A1 MANAGING EXECUTION OF MIXED WORKLOADS IN A SIMULTANEOUS MULTI-THREADED VIRTUAL ADDRESSING REGISTER FILE	20071227 718/102		
US 20070202967 29 A1 BUILDING A WAVECACHE	20071227 345/559		
US 2007020271556 A1 System and method for leveraging independent innovation in entertainment content and graphics	20071122 717/151		
US 2007020234284 A1 Multithreaded dynamic voltage-frequency scaling microprocessor	20071004 717/109		
US 2007020234091 A1 Software self-defense systems and methods	20071004 713/322		
US 2007020234070 A1 Method and system for trace generation using memory index hashing	20071004 713/190		
US 2007020234016 A1 Scheduling in a multicore processor	20071004 712/227		
US 2007020220517 A1 Managing power consumption in a multicore processor	20070920 718/102		
US 2007020220294 A1 Data Processing Architectures	20070920 713/320		
US 2007020220232 A1 Branching and Behavioral Partitioning for a VLIW Processor	20070920 712/220		
US 2007020219771 A1 Data Processing Architectures	20070920 703/15		
US 2007020217453 A1 ACROSS-THREAD OUT-OF-ORDER INSTRUCTION DISPATCH IN A MULTITHREADED INSTRUCTION DISPATCH	20070920 370/503		
US 2007020214343 A1 Compact linked-list-based multi-threaded instruction graduation buffer	20070913 712/220		
US 2007020204139 A1 Multi-threading processors, integrated circuit devices, systems, and processes of operation and Mask-Pattern Determination Using Topology Types	20070830 712/218		
US 20070186208 A1 SYNCHRONIZED STORAGE PROVIDING MULTIPLE SYNCHRONIZATION SEMANTICS	20070830 712/214		
US 20070186028 A2 Lithography Verification Using Guard Bands	20070809 716/21		
US 20070184369 A1 Managing and enhancing execution over parallel pipelines	20070809 711/1		
US 20070174593 A1 Method and apparatus for vessel characterization	20070809 430/30		
US 20070167751 A1 Queued locks using monitor-memory wait	20070726 712/220		
US 20070162774 A1 SYSTEMS, METHODS AND APPARATUS FOR AUTOMATA LEARNING IN GENERATION	20070719 600/437		
US 20070162410 A1 METHOD AND SYSTEM FOR PROVIDING ON-LINE INTERACTIVITY OVER A SERVER-C	20070712 713/300		
US 20070136374 A1 Partitioning of tasks for execution by a VLIW hardware acceleration system	20070712 706/48		
US 20070129924 A1 SYSTEM AND METHOD FOR IMPLEMENTING A CLIENT SIDE HTTP STACK	20070614 707/104.1		
US 20070118596 A1 Pulse-signaling circuits for networks on chip	20070607 703/14		
US 20070115040 A1 MULTITHREADING INSTRUCTION SCHEDULER EMPLOYING THREAD GROUP PRIORITIZATION	20070524 709/203		
US 20070113053 A1 SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-IND	20070517 712/214		
US 20070106990 A1 SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-IND	20070510 718/102		
US 20070106989 A1 SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-IND	20070510 718/102		
US 20070106988 A1 SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-IND	20070510 718/102		
US 20070106887 A1 Video game system using pre-encoded digital audio mixing	20070510 712/228		
US 20070105631 A1 BARREL-INCREMENTER-BASED ROUND-ROBIN APPARATUS AND INSTRUCTION DISPATCH	20070510 463/42		
US 20070089112 A1 Analyzing the behavior of a storage system	20070419 718/100		
US 20070088861 A1 Generic, multi-instance method and GUI detection system for tracking and monitoring computer	20070412 715/709		
US 20070083813 A1 Storage of key in non-volatile memory	20070412 707/3		
US 20070083491 A1 SYSTEMS, METHODS AND APPARATUS FOR MODELING, SPECIFYING AND DEPLOYING	20070329 717/136		
US 20070074182 A1 Systems, Methods and Apparatus for Procedure Development and Verification	20070329 717/136		
US 20070074180 A1 Per thread buffering for storing profiling data	20070329 717/127		
US 20070074171 A1 SYSTEMS, METHODS AND APPARATUS FOR PATTERN MATCHING IN PROCEDURE DEVELOPMENT	20070322 717/135		
US 20070067755 A1 SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-IND	20070222 718/108		

US 20070044105 A2	SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-IND	20070222	718/108
US 20070043935 A2	SYMMETRIC MULTIPROCESSOR OPERATING SYSTEM FOR EXECUTION ON NON-IND	20070222	712/228
US 20070038429 A1	System simulation method	20070215	703/14
US 20070019016 A1	Printer comprising a printhead and at least two printer controllers connected to a common input	20070125	347/13
US 20070010329 A1	Video game system using pre-encoded macro-blocks	20070111	463/42
US 20070009043 A1	Video game system using pre-encoded macro-blocks and a reference grid	20070111	375/240.24
US 20070009042 A1	Video game system using pre-encoded macro-blocks in an I-frame	20070111	375/240.24
US 20070009036 A1	Video game system having an infinite playing field	20070111	375/240.16
US 20070009035 A1	Video game system using pre-generated motion vectors	20070111	375/240.16
US 20070009029 A1	Video encoder with latency control	20070111	375/240.12
US 20070006231 A1	Mechanism for instruction set based thread execution on a plurality of instruction sequencers	20070104	718/100
US 20070006171 A1	System development tool	20070104	717/131
US 20060294347 A1	Programmable event driven yield mechanism which may activate service threads	20061228	712/244
US 20060294312 A1	Generation sequences	20061228	711/122
US 20060282839 A1	Mechanism for monitoring instruction set based thread execution on a plurality of instruction seq	20061214	719/318
US 20060274112 A1	Printhead comprising different printhead modules	20061207	347/42
US 20060253271 A1	Method for facilitating transformation of multi-threaded process-oriented object code to event-ba	20061109	703/15
US 20060248527 A1	Platform independent replication	20061102	718/1
US 20060236136 A1	Apparatus and method for automatic low power mode invocation in a multi-threaded processor	20061019	713/300
US 20060236135 A1	Apparatus and method for software specified power management performance using low power	20061019	713/300
US 20060230317 A1	System and method for benchmarking	20061012	714/38
US 20060221980 A1	Method and apparatus for a high efficiency two-stage rotating priority arbiter with predictable arb	20061005	370/400
US 20060206692 A1	Instruction dispatch scheduler employing round-robin apparatus supporting multiple thread prior	20060914	712/215
US 20060206635 A1	DMA engine for protocol processing	20060914	710/22
US 20060195883 A1	Symmetric multiprocessor operating system for execution on non-independent lightweight threat	20060831	712/228
US 20060190946 A1	Symmetric multiprocessor operating system for execution on non-independent lightweight threat	20060824	718/108
US 20060190945 A1	Symmetric multiprocessor operating system for execution on non-independent lightweight threat	20060824	718/108
US 20060187251 A1	Printer controller for supplying data to a printhead module having interleaved shift registers	20060824	347/13
US 20060184769 A1	Localized generation of global flush requests while guaranteeing forward progress of a process	20060817	712/216
US 20060181558 A1	Printhead module having horizontally grouped firing order	20060817	347/12
US 20060179439 A1	Leaky-bucket thread scheduler in a multithreading microprocessor	20060810	718/105
US 20060179429 A1	Building a wavecache	20060810	717/151
US 20060179284 A1	Multithreading microprocessor with optimized thread scheduler for increasing pipeline utilization	20060810	712/219
US 20060179283 A1	Return data selector employing barrel-incrementer-based round-robin apparatus	20060810	712/218
US 20060179281 A1	Multithreading instruction scheduler employing thread group priorities	20060810	712/214
US 20060179280 A1	Multithreading processor including thread scheduler based on instruction stall likelihood predicti	20060810	712/214
US 20060179279 A1	Bifurcated thread scheduler in a multithreading microprocessor	20060810	712/214
US 20060179276 A1	Fetch director employing barrel-incrementer-based round-robin apparatus for use in multithreadi	20060810	712/205
US 20060179274 A1	Instruction/skid buffers in a multithreading microprocessor	20060810	712/205
US 20060179207 A1	Processor instruction retry recovery	20060810	711/100
US 20060179194 A1	Barrel-incrementer-based round-robin apparatus and instruction dispatch scheduler employing s	20060810	710/111
US 20060168399 A1	Automatic generation of software-controlled caching and ordered synchronization	20060727	711/118
US 20060164462 A1	Print controller for supplying data to a printhead comprising different printhead modules	20060727	347/40
US 20060164455 A1	Printhead module having operation controllable on basis of thermal sensors	20060727	347/14
US 20060164454 A1	Method for at least partially compensating for errors in ink dot placement due to erroneous rotati	20060727	347/14
US 20060164453 A1	Printhead module having nozzle redundancy	20060727	347/13
US 20060164452 A1	Printer controller for supplying data to a printhead capable of printing a maximum of n channels	20060727	347/12

US 20060164451 A1	Method of expelling ink from nozzles in groups, alternately, starting at outside nozzles of each group	20060727 347/12
US 20060161921 A1	Preemptive multitasking employing software emulation of directed exceptions in a multithreading processor	20060720 718/102
US 20060161421 A1	Software emulation of directed exceptions in a multithreading processor	20060720 703/26
US 20060150183 A1	Mechanism to emulate user-level multithreading on an OS-sequestered sequencer	20060706 718/100
US 20060143454 A1	Storage of multiple keys in memory	20060629 713/170
US 20060143350 A1	Apparatus, method and system for aggregating computing resources	20060629 710/242
US 20060139681 A1	Use of variant and base keys with three or more entities	20060629 358/1.14
US 20060139388 A1	Printer controller for supplying dot data to at least one printhead module having faulty nozzle	20060629 347/13
US 20060139387 A1	Printer controller for providing data and command via communication output	20060629 347/13
US 20060139386 A1	Printhead module having nozzle redundancy for faulty nozzle tolerance	20060629 347/12
US 20060139380 A1	Printer controller for causing expulsion of ink from nozzles in groups, starting at outside nozzles	20060629 347/5
US 20060136725 A1	Use of variant and base keys with two entities	20060622 713/171
US 20060132822 A1	Storage of program code in arbitrary locations in memory	20060622 358/1.14
US 20060132525 A1	Printer controller for at least partially compensating for erroneous rotational displacement	20060622 347/19
US 20060132521 A1	Printer controller for controlling a printhead with horizontally grouped firing order	20060622 347/14
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US 20060130010 A1	Model checking with bounded context switches	20060615 717/136
US 20060129906 A1	Component models	20060615 715/500
US 20060129806 A1	Key transportation	20060615 713/161
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US 20060125863 A1	Printer controller for controlling a printhead module based on thermal sensing	20060615 347/14
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US 20060125858 A1	Printer controller for supplying data to a printhead module having one or more redundant nozzle	20060615 347/12
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US 20060125855 A1	Printer controller for supplying data to one or more printheads via serial links	20060615 347/9
US 20060125854 A1	Printhead module having two shift registers	20060615 347/9
US 20060110199 A1	PRINTER COMPRISING TWO UNEVEN PRINthead MODULES AND AT LEAST TWO PRINTER CONTROLLERS	20060525 400/62
US 20060100845 A1	Multiple stream real time data simulation adapted for a KStore data structure	20060511 703/22
US 20060098044 A1	Printhead module having a dropped row	20060511 347/43
US 20060098042 A1	Method of manufacturing left-handed and right-handed printhead modules	20060511 347/40
US 20060095606 A1	Method, system and storage medium for lockless InfiniBand™ Poll for I/O completion	20060504 710/46
US 20060092222 A1	Printhead module for expelling ink from nozzles in groups, alternately, starting at outside nozzles	20060504 347/43
US 20060092205 A1	Printhead module for expelling ink from nozzles in groups, starting at outside nozzles of each group	20060504 347/13
US 20060087525 A1	Method of expelling ink from nozzles in groups, starting at outside nozzles of each group	20060427 347/12
US 20060085462 A1	Method, apparatus, and computer program product for processing a queue of messages	20060420 707/100
US 20060072952 A1	Printhead formed with offset printhead modules	20060406 400/62
US 20060067592 A1	Configurable image processor	20060330 382/303
US 20060061795 A1	Storage of key in arbitrary locations in memory	20060323 358/1.14
US 20060047873 A1	Scalable, two-stage round robin arbiter with re-circulation and bounded latency	20060302 710/243
US 20060013286 A1	Configurable multimode despreaders for spread spectrum applications	20060119 375/141
US 20060002412 A1	Methods and apparatus for supporting programmable burst management schemes on pipelined	20060105 370/412
US 20050289396 A1	Conditional breakpoint using breakpoint function and breakpoint command	20051229 714/34
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US 20050257083 A1	Transaction-based storage system and method that uses variable sized objects to store data	20051117 714/6
US 20050251639 A1	Smart memory based synchronization controller for a multi-threaded multiprocessor SoC	20051110 711/168
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US 20050251040 A1	Advanced application framework system and method for use with a diagnostic medical ultrasound	20051110 600/437
US 20050235075 A1	Method of and device for changing an output rate	20051020 710/52
US 20050210275 A1	Software self-defense systems and methods	20050922 713/190
US 20050204348 A1	Software self-defense systems and methods	20050915 717/140
US 20050204316 A1	Predictable design of low power systems by pre-implementation estimation and optimization	20050915 716/2
US 20050183072 A1	Software self-defense systems and methods	20050818 717/140
US 20050177816 A1	Automatic generation of graphical program code for a graphical program based on the target platform	20050811 717/105
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US 20050149697 A1	Mechanism to exploit synchronization overhead to improve multithreaded performance	20050707 712/214
US 20050138328 A1	Cross-thread out of order instruction dispatch in a multithreaded graphics processor	20050623 712/205
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US 20050125795 A1	Integrated mechanism for suspension and deallocation of computational threads of execution in a multiprocessor system	20050609 718/100
US 20050125629 A1	Mechanisms for dynamic configuration of virtual processor resources	20050609 712/1
US 20050108710 A1	System and method for implementing a client side HTTP stack	20050519 718/100
US 20050086335 A1	Method and apparatus for automatic modeling building using inference for IT systems	20050421 709/223
US 20050081117 A1	Safety controller with safety response time monitoring	20050414 714/47
US 20050050395 A1	Mechanisms for assuring quality of service for programs executing on a multithreaded processor	20050303 714/38
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US 20050049849 A1	Cross-platform virtual tape device emulation	20050303 703/26
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US 20040193828 A1	Memory recycling in computer systems	20040930 711/170
US 20040186921 A1	Memory mapping in a multi-engine processor	20040923 710/1
US 20040186379 A1	Diagnostic medical ultrasound system having a pipes and filters architecture	20040923 600/437
US 20040163083 A1	Programmable event driven yield mechanism which may activate other threads	20040819 718/102
US 20040148603 A1	Technique for reaching consistent state in a multi-threaded data processing system	20040729 718/100
US 20040107169 A1	METHOD AND APPARATUS FOR GENERATING AND DISTRIBUTING PERSONALIZED MEDIA	20040603 705/59
US 20040073905 A1	Method and apparatus to quiesce a portion of a simultaneous multithreaded central processing unit	20040415 718/101
US 20040066584 A1	Array of parallel programmable processing engines and deterministic method of operating the same	20040108 718/107
US 20030177187 A1	Computing grid for massively multi-player online games and other multi-user immersive persistent environments	20030918 709/205
US 20030172205 A1	Methods and components for mechanical computer	20030911 710/45
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US 20030126379 A1	Instruction sequences for suspending execution of a thread until a specified memory access occurs	20030703 711/150
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US 20030126186 A1	Method and apparatus for suspending execution of a thread until a specified memory access occurs	20030703 718/107
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US 20030046645 A1	Monitor manager that creates and executes state machine-based monitor instances in a digital system	20030306 716/5
US 20030041163 A1	Data processing architectures	20030227 709/232
US 20030041090 A1	Yield on multithreaded processors	20030227 718/106

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US 20020124085 A1	Method of simulating operation of logical unit, and computer-readable recording medium retaining	20020905	709/226
US 20020107809 A1	System and method for licensing management	20020808	705/59
US 20020091736 A1	Component models	20020711	715/513
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US 20020038451 A1	System and method for leveraging independent innovation in entertainment content and graphics	20020328	717/105
US 20020038301 A1	Speculative caching of individual fields in a distributed object system	20020328	707/10
US 20010040915 A1	Configurable multimode despreaders for spread spectrum applications	20011115	375/150
US 7314261 B2	Printhead module for expelling ink from nozzles in groups, alternately, starting at outside nozzles	20080101	347/9
US 7310722 B2	Across-thread out of order instruction dispatch in a multithreaded graphics processor	20071218	712/207
US 7296256 B2	Method and apparatus for automatic modeling building using inference for IT systems	20071113	717/104
US 7293260 B1	Configuring methods that are likely to be executed for instrument-based profiling at application r	20071106	717/130
US 7293259 B1	Dynamically configuring selected methods for instrument-based profiling at application run-time	20071106	717/130
US 7290852 B2	Printhead module having a dropped row	20071106	347/40
US 7290116 B1	Level 2 cache index hashing to avoid hot spots	20071030	711/216
US 7281777 B2	Printhead module having a communication input for data and control	20071016	347/9
US 7281330 B2	Method of manufacturing left-handed and right-handed printhead modules	20071016	29/890.1
US 7275805 B2	Printhead comprising different printhead modules	20071002	347/42
US 7267417 B2	Printer controller for supplying data to one or more printheads via serial links	20070911	347/13
US 7266661 B2	Method of storing bit-pattern in plural devices	20070904	711/164
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US 7243193 B2	Storage of program code in arbitrary locations in memory	20070710	711/154
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US 7219346 B2	System and method for implementing a client side HTTP stack	20070515	718/102
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US 7213093 B2	Queued locks using monitor-memory wait	20070501	710/200
US 7200699 B2	Scalable, two-stage round robin arbiter with re-circulation and bounded latency	20070403	710/120
US 7188928 B2	Printer comprising two uneven printhead modules and at least two printer controllers, one of whi	20070313	347/40
US 7163345 B2	Printhead having printhead modules vertically offset at join region	20070116	400/62
US 7127561 B2	Coherency techniques for suspending execution of a thread until a specified memory access oc	20061024	711/145
US 7123696 B2	Method and apparatus for generating and distributing personalized media clips	20061017	379/88.16
US 7117048 B2	Safety controller with safety response time monitoring	20061003	700/79
US 7103873 B2	System and method for leveraging independent innovation in entertainment content and graphics	20060905	717/109
US 7093989 B2	Printer comprising two uneven printhead modules and at least two printer controllers, one which	20060822	400/62
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US 6987517 B1	Programmable graphics processor for generalized texturing	20060117	345/582
US 6983357 B2	Hardware accelerator for an object-oriented programming language	20060103	712/34
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US 6981110 B1	Hardware enforced virtual sequentiality	20051227	711/154
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US 6961925 B2	Parallelism performance analysis based on execution trace information	20051101	717/128
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US 6618737 B2	Speculative caching of individual fields in a distributed object system	20030909 707/205
US 6574653 B1	Blackboard-centric layered software architecture	20030603 719/310
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US 6442585 B1	Method for scheduling contexts based on statistics of memory system interactions in a computer	20020827 718/108
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US 6366947 B1	System and method for accelerating network interaction	20020402 709/203
US 6332178 B1	Method for estimating statistics of properties of memory system transactions	20011218 711/118
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US 6324492 B1	Server stress testing using multiple concurrent client simulation	20011127 703/13
US 6314471 B1	Techniques for an interrupt free operating system	20011106 710/5
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US 6240502 B1	Apparatus for dynamically reconfiguring a processor	20010529 712/15
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US 6237059 B1	Method for estimating statistics of properties of memory system interactions among contexts in :	20010522 711/100
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US 5996076 A	System, method and article of manufacture for secure digital certification of electronic commerce	19991130 713/156
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US 5890008 A	Method for dynamically reconfiguring a processor	19990330 712/15
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US 5809450 A	Method for estimating statistics of properties of instructions processed by a processor pipeline	19980915 702/186
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US 5694603 A	Computer memory product with preemptive multithreading software	19971202 718/107
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US 5515538 A	Apparatus and method for interrupt handling in a multi-threaded operating system kernel	19960507 710/260
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US 5337412 A	Method and apparatus for substituting real and virtual devices independent from an data processing	19940809 719/324
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